

In re Application of:
Regula, Jack
Serial No. 08/771,581
Filed December 20, 1996

JC503 U.S. PTO
09/421771
10/19/99

Attached are Information Disclosure Statement Reference Sheets ⁴ ~~25~~ from Parent Application
S/N 08/771,581, Filed December 20, 1996.

If needed, actual references will be provided if requested.



Art Cited

United States Patent Numbers:

5,764,924

Foreign Patent Documents:

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(37 CFR 1.98(b))

SERIAL NO.

OUP ART UNIT

FILING DATE

APPLICANT(S)

Jack Regula

ATTY. DOCKET NO.

SS-696-01

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
A						
B						
C						
D						
E						
F						
G						
H						
I						
J						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
L						
M						
N						
O						
P						
Q						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R	Master Thesis by IVAN TVING, August 28, 1994
	"Multiprocessor interconnection using SCI"
S	PCI Local Bus Specification, Revision 2.1, June '91, 1995
	PCI Special Interest Group
T	PCI to PCI Bridge Architecture Specification, Version 1.0, 1994
	PCI Special Interest Group

EXAMINER

DATE

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.